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12400 WILSHIRE BOULEVARD
SEVENTH FLOOR
LOS ANGELES CA 90025-1030

Patent No. : 7,007,130 B1
Inventor(s) : Thomas J. Holman
Issued : February 28, 2006
For **MEMORY SYSTEM INCLUDING A
MEMORY MODULE HAVING A MEMORY
MODULE CONTROLLER INTERFACING
BETWEEN A SYSTEM MEMORY
CONTROLLER AND MEMORY DEVICES OF
THE MEMORY MODULE**
Doc. No. **042390.P5346**

To Whom It May Concern:

It has been brought to the attention of the Certificate of Correction Branch that Patent No. 7,007,130 issued on February 28, 2006 contained several errors in the printed claims.

The patent was not printed with the correct set of claims. The patent was printed with a previously submitted amendment dated March 24, 2005 instead of the latest amendment dated August 12, 2005 as amended by the Examiner's Amendment dated September 13, 2005.

Therefore, a certificate of correction will be issued to incorporate the changes set forth in the amendment dated August 12, 2005 and Examiner's Amendment dated September for claims 1-20 of the printed patent.

No further response is required, from applicants (attorney). However, errors discovered by attorney, other than as noted and described above, should be noted on a *copy* of the Certificate of Correction that was issued in error, accompanied by a signed transmittal letter and submitted directed to this Branch.

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